

LDO voltage regulator (output voltage 2.7 V)

SPECIFICATION

1 FEATURES

- SMIC CMOS 0.18 μm
- Output voltage 2.7 V
- High precision stabilization voltage
- Several operating modes
- Low current consumption
- Small area
- Low output level vs. supply voltage in a wide range
- Portable to other technologies (upon request)

2 APPLICATION

The output voltage can be used for both analog and digital blocks. The main application:

- Portable electronic devices
- System-on-chip for different purposes
- Measurement and calibration systems
- Navigation systems
- Communication systems

3 OVERVIEW

The regulator stabilizes the external voltage (supply voltage) and forms an output level of the specified value. Adjustment is carried out by positive feedback through the operational amplifier. Feedback allows for voltage drop on the wire by which stabilized voltage is supplied to the cell. The block is fabricated on SMIC CMOS 0.18 μm technology.

4 STRUCTURE

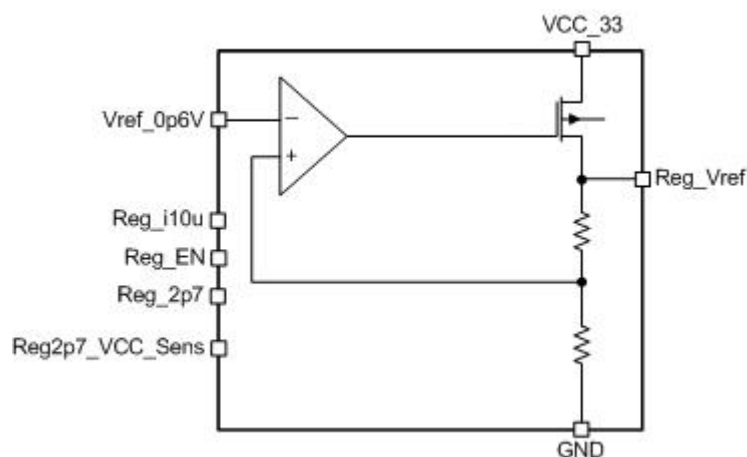


Figure 1: LDO voltage regulator structure

5 PIN DESCRIPTION

Name	Direction	Description
Reg_i10u	I	Reference current 10 uA
Reg_EN	I	Enable/disable
Reg_2p7	I	Output voltage control
Reg_2p7_VCC_Sens	I	Feedback output
Vref_0p6V	O	Reference voltage
Reg_Vref	O	Output voltage
VCC_33	IO	Supple voltage 3.15 V
GND	IO	Ground

6 LAYOUT DESCRIPTION

The block dimensions are given in the table 1.

Table 1: Block dimensions

Dimension	Value	Unit
Height	167.31	um
Width	301.62	um

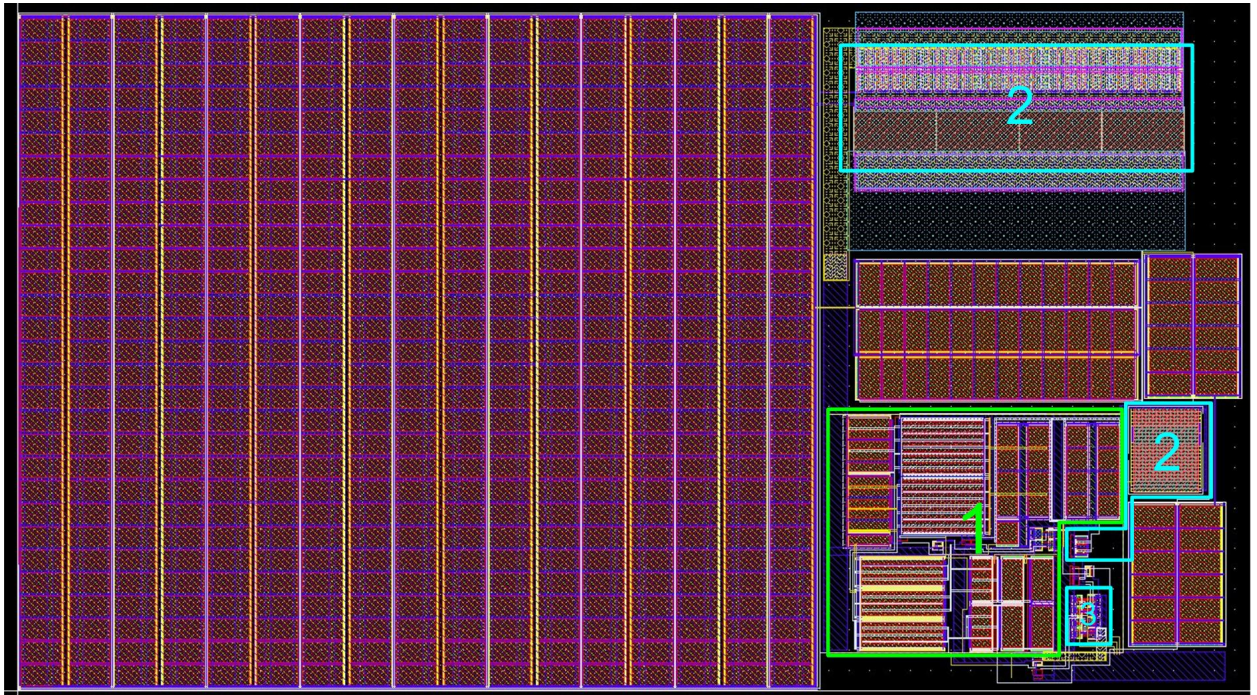


Figure 2: Device layout

1. OTA
2. Basic control elements
3. Control logic

7 OPERATING CHARACTERISTICS

7.1 TECHNICAL CHARACTERISTICS

Technology _____ SMIC CMOS 0.18 um
 Status _____ silicon proven
 Area _____ 0.05 mm²

7.2 ELECTRICAL CHARACTERISTICS

The values of electrical characteristics are specified for $V_{cc} = 2.7 \div 3.6$ V and $T_j = -45 \div +90$ °C. Typical values are at $V_{cc} = 3.15$ V and $T_j = +27$ °C, unless otherwise specified.

Parameter	Symbol	Condition	Value			Unit
			min	typ.	max	
Supply voltage	V_{cc}	-	2.7	3.15	3.6	V
Operating temperature range	T_j	-	-45	27	90	°C
Reference voltage	V_R	-	-	0.6	-	V
Voltage regulator output level	V_{ref}	-	2.61	2.7	2.75	V
Reference current	I_R	-	-	10	-	uA
Maximum load current	I_L	-	-	10	-	mA
Reference voltage deviation	ΔV_{ref}	-	-	3.28	-	%
Temperature error	ΔT	-	-	1.26	-	%
Current consumption (external supply voltage)	I_{cc}	-	-	42.3	48.5	uA
Current consumption in a standby mode	I_{stb}	-	-	0.07	0.5	nA
Input logic-level high	V_{IH}	For digital inputs	$0.7 V_{cc}$	-	3.6	V
Input logic-level low	V_{IL}		-0.25	-	0.3	V

8 DELIVERABLES

Depending on license type IP may include:

- Schematic or NetList
- Abstract view (.lef and .lib files)
- Layout (optional)
- Verilog behavior model
- Extracted view (optional)
- GDSII
- DRC, LVS, antenna report
- Test bench with saved configurations (optional)
- Documentation