

Wideband LNA with a gain adjustment range from -22 to 20dB

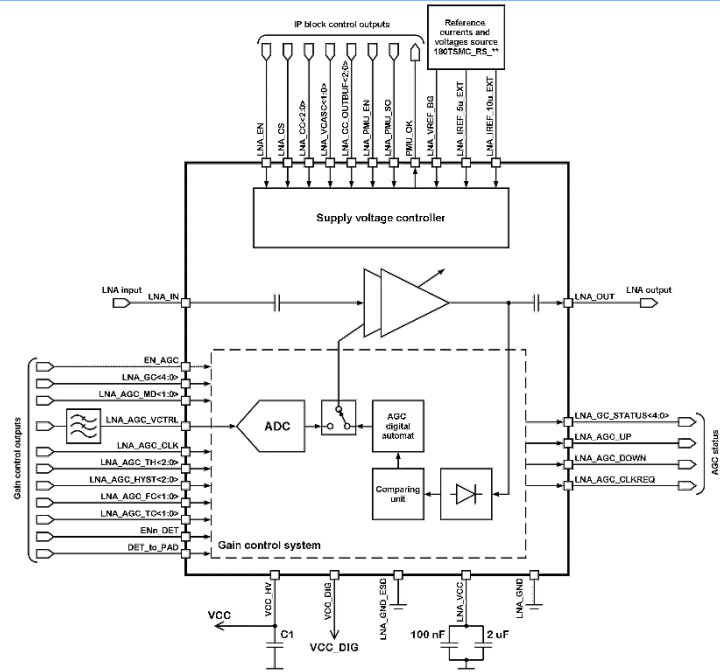
OVERVIEW

180TSMC_LNA_14 is a IP block which includes a low-noise amplifier (LNA), gain control system and supply voltage controller. The low-noise amplifier is intended to amplify wideband high-frequency signal at the receiver input, provides a wide range of gain control and low noise figure as well as has high linearity characteristics. The supply voltage controller is designed to generate internal reference currents and indicate increased current consumption.

IP technology: TSMC SiGe BiCMOS 180nm.

IP status: silicon proven.

Area: 0.98mm².



ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Conditions	Value			Unit
			min	typ.	max	
Supply voltage	V _{CC}	-	2.97	3.3	3.63	V
Operating temperature range	T _J	Nominal	-40	+40	+100	°C
Current consumption	I _{CC}	Operating mode	-	18	-	mA
		Standby mode	-	100	-	µA
		Shutdown mode	-	380	-	nA
Input logic-level low	V _{IL}	-	0	-	0.2V _{CC}	V
Input logic-level high	V _{IH}	-	0.8V _{CC}	-	3.6	V
Output logic-level low	V _{OL}	Load current 299µA	0	-	0.4	V
Output logic-level high	V _{OH}	Load current 287µA	V _{CC} -0.4	-	V _{CC}	V
Input reference current (inflow)	I _{5u}	-	4.9	-	5.4	µA
Input reference current (inflow)	I _{10u}	-	9.8	-	10.7	µA
Reference voltage	V _{REF}	-	1.14	-	1.21	V
LNA input DC level	V _{IN DC}	AC coupling	-0.4	-	4	V
LNA output DC level	V _{OUT DC}	AC coupling	-0.4	-	4	V
Operating frequency range	F _{IN}	-	500	-	1500	MHz
Gain	G	Minimum	-	-22	-	dB
		Maximum	17.5	20.0	21.4	
Gain adjustment step	ΔG _{STEP}	-	0.4	1.5	2.5	dB
Noise figure	NF	G = 20dB	1.4	2	3.3	dB
		G = 0dB	-	16	-	
Input compression point	IP _{1dB}	G = 20dB	-20.6	-17.2	-14.4	dBm
		G = -22dB	-	21	-	
3 rd order intermodulation point	I _{IP3}	G = 20dB	-7.6	0	2.6	dBm
		G = -22dB	-	30	-	
3 rd order intermodulation distortions	IM3	P _{IN} = -45dBm; P _{OUT} = -25dBm	-	90	-	dB
		P _{IN} = -40dBm; P _{OUT} = -25dBm	-	75	-	
		P _{IN} = -3dBm; P _{OUT} = -25dBm	-	65	-	
Multi-tone power ratio	MTPR	-	-	44.4	-	dB
Input VSWR	VSWR _{IN}	50Ω	-	-	2.3	-
Output VSWR	VSWR _{OUT}	50Ω	-	-	2	-