

Fifth-order passive LPF

SPECIFICATION

1 FEATURES

- TSMC CMOS 0.065 μm
- Differential inputs, outputs
- Fixed cut-off frequency: 25MHz, 50MHz, 100MHz
- High linearity
- Low noise figure
- Supported foundries: TSMC, UMC, Global Foundries, SMIC

2 APPLICATION

- IF signal processing

3 OVERVIEW

The 5th order passive differential low-pass filter (LPF) with fixed cut-off frequency presets. LPF is implemented using integrated inductors. Cut-off frequency switching is performed by switched-capacitor circuit.

4 STRUCTURE

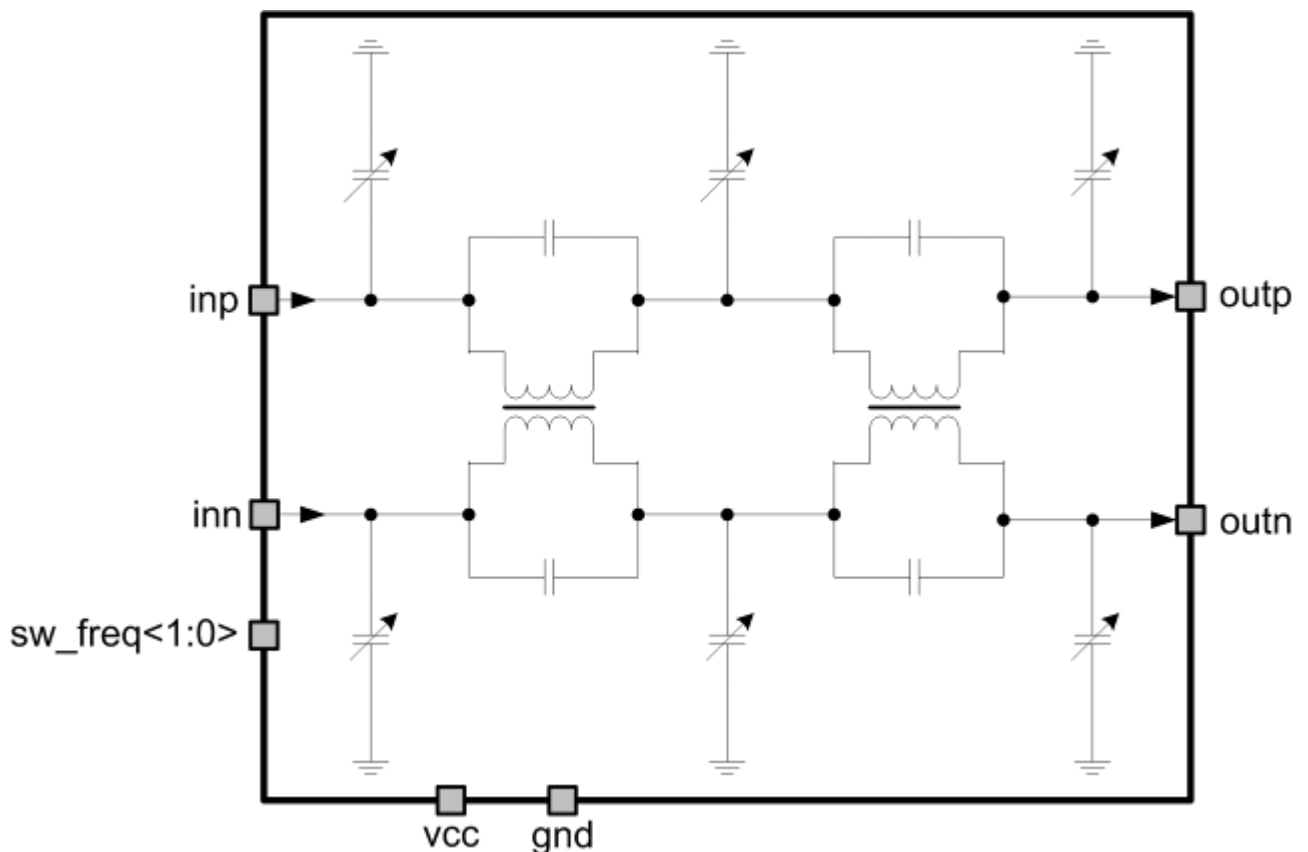


Figure 1: Fifth-order passive LPF structure.

5 PIN DESCRIPTION

Name	Direction	Description
sw_freq<1:0>	I	LPF cut-off frequency control code
inp	I	LPF differential input
inn	I	
outp	O	LPF differential output
outn	O	
vcc	IO	Supply voltage
gnd	IO	Ground

6 LAYOUT DESCRIPTION

The block dimensions are given in the table 1.

Table 1: Block dimensions.

Dimension	Value	Unit
Height	1355	μm
Width	1740	μm

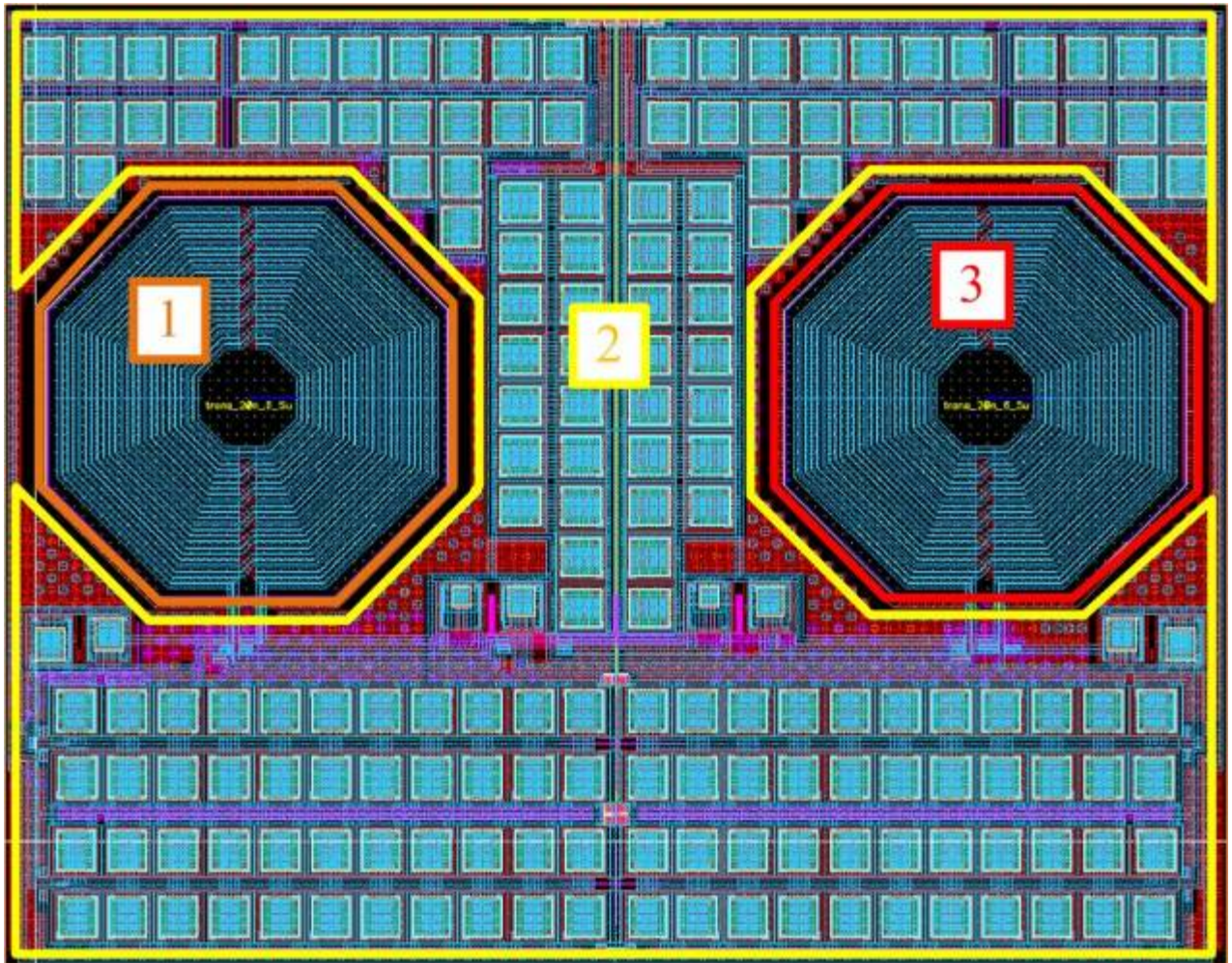


Figure 2: Device layout view.

1. Transformer №1
2. Switched capacitors
3. Transformer №2

7 OPERATING CHARACTERISTICS

7.1 TECHNICAL CHARACTERISTICS

Technology _____ TSMC CMOS CRN65LP
 Status _____ silicon proven
 Area _____ 2.36mm²

7.2 ELECTRICAL CHARACTERISTICS

The values of electrical characteristics are specified for $V_{cc} = 2.375 \div 2.625$ V and $T = -40 \div +125^{\circ}\text{C}$. Typical values are at $V_{cc} = 2.5\text{V}$, $T = +85^{\circ}\text{C}$, unless otherwise specified.

Parameter	Symbol	Condition	Value			Unit
			min	typ	max	
Supply voltage	V_{cc}	-	2.375	2.5	2.625	V
Temperature range	T	-	-40	85	125	$^{\circ}\text{C}$
Filter order	k	-	-	5	-	-
Insertion loss	G	-	-	-3.7	-	dB
Input signal bandwidth	F_C	Preset 1	-	25	-	MHz
		Preset 2	-	50	-	
		Preset 3	-	100	-	
Attenuation >400MHz	G_A	$F_C = 100$ MHz	48	50	51	dB
		$F_C = 50$ MHz	61	64	68	
		$F_C = 25$ MHz	69	73	78	
Amplitude ripple	ΔA	$F_C = 100$ MHz	0.7	1.1	1.7	dB
		$F_C = 50$ MHz	1.5	2.4	3	
		$F_C = 25$ MHz	2	3	3.9	
Noise figure	NF	-	-	8	8.1	dB
1 dB compression point	P_{1dB}	-	14	14	-	dBm
Input/output impedance	R	Differential	-	50	-	Ω
Current consumption	I_{cc}	-	-	-	1	μA
Input logic-high level	V_{IH}	For digital input	0.85V _{dd}	-	1.15V _{dd}	V
Input logic-low level	V_{IL}		-0.2	-	+0.2	V

8 TYPICAL CHARACTERISTICS

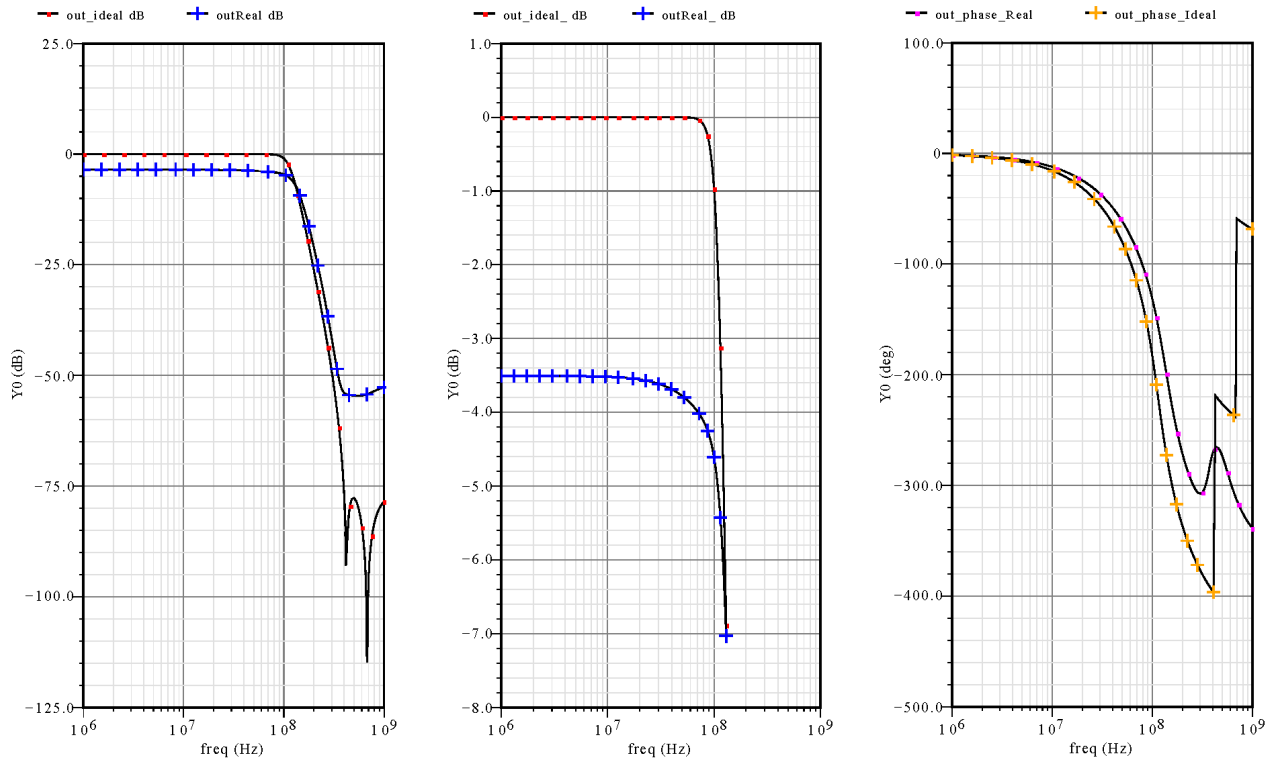


Figure 3: Amplitude frequency characteristics and phase frequency variation for band pass 100MHz.

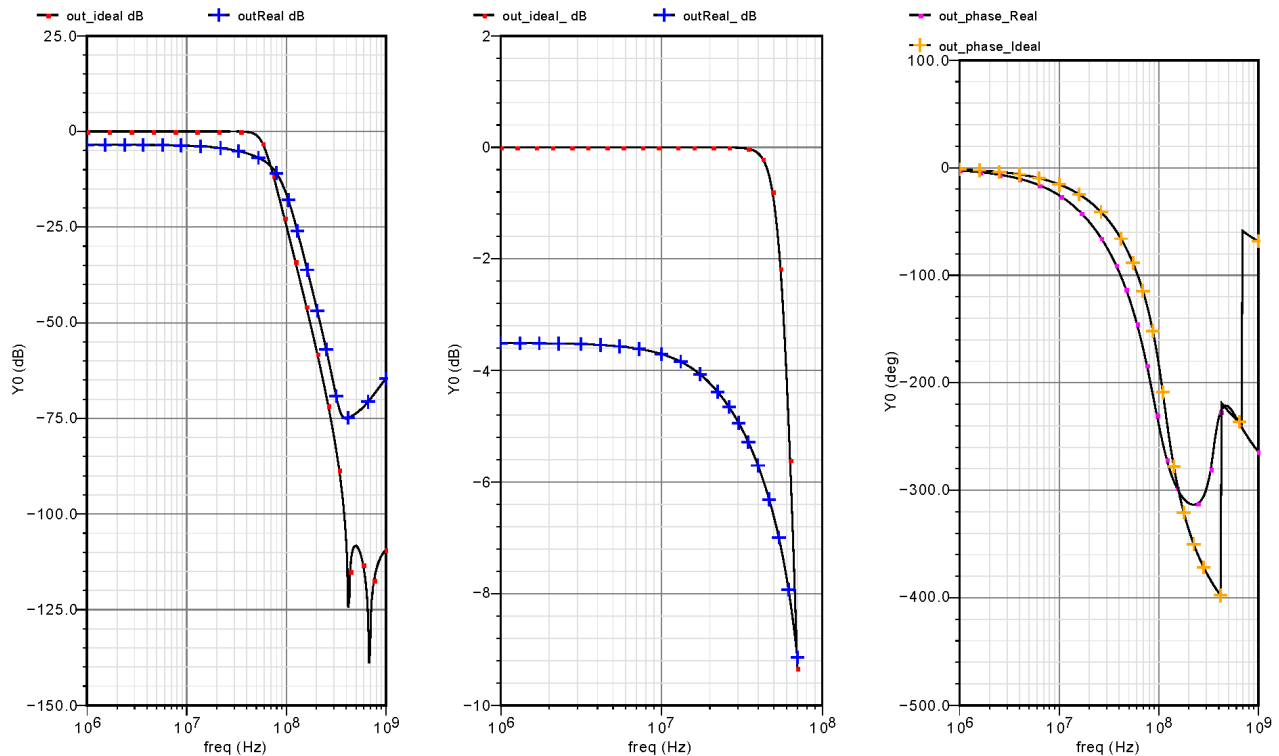


Figure 4: Amplitude frequency characteristics and phase frequency variation for band pass 50MHz.

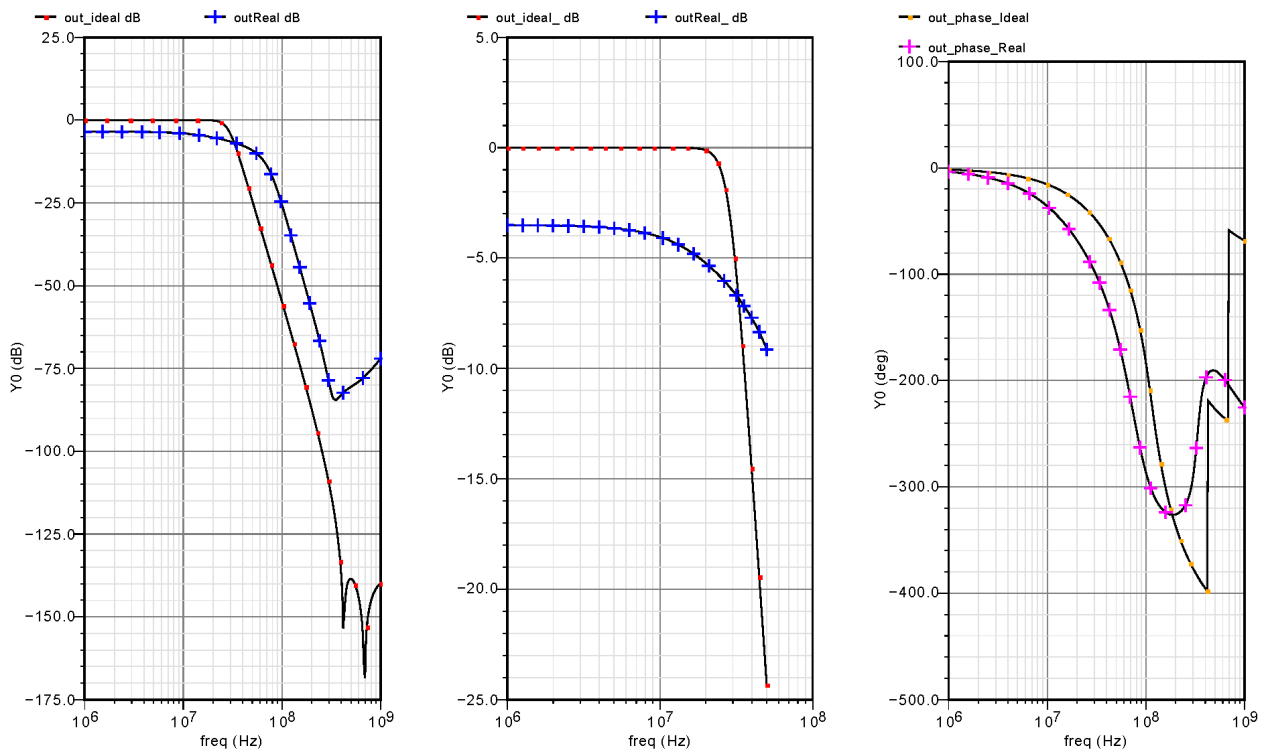


Figure 5: Amplitude frequency characteristics and phase frequency variation for band pass 25MHz.

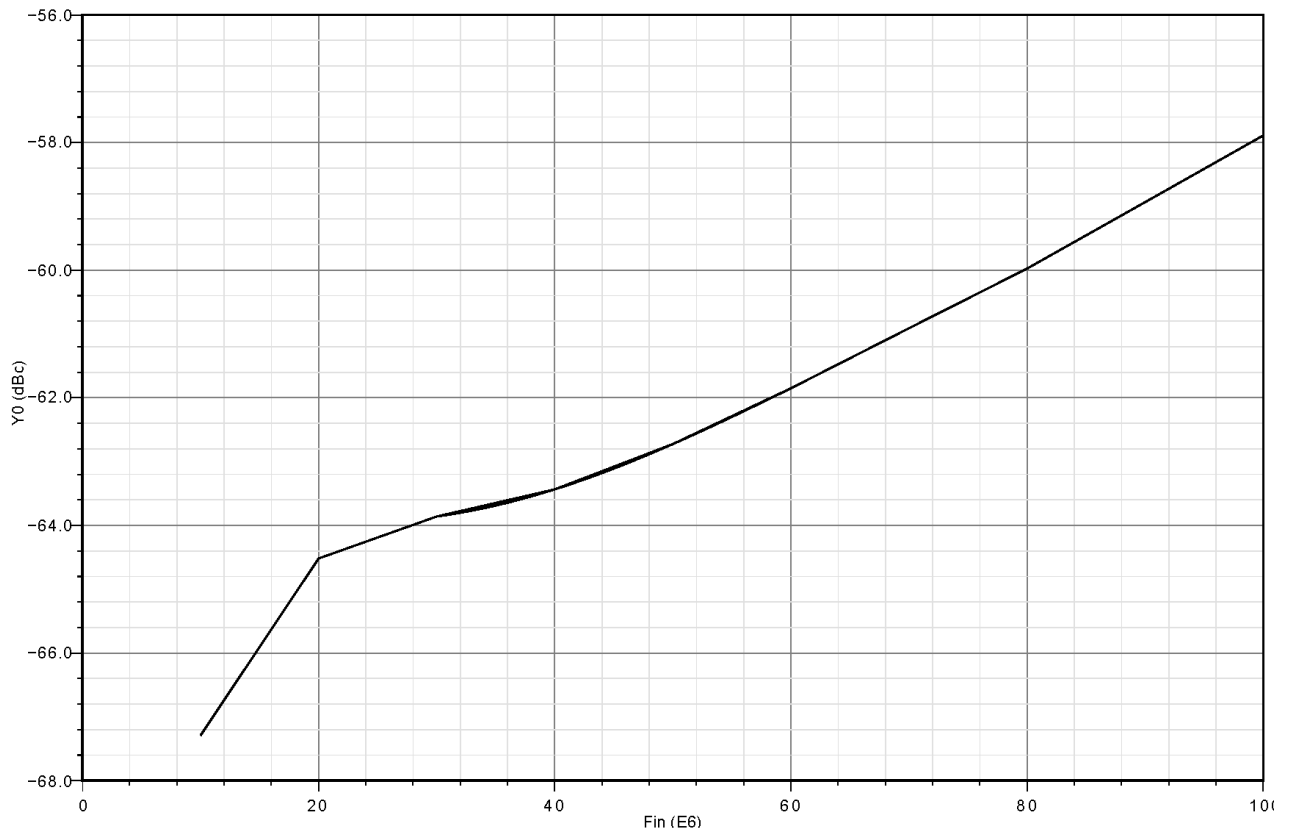


Figure 6: IM3, $V_{out}=2V$ (peak-peak).

9 DELIVERABLES

IP contents:

- Schematic or NetList
- Layout or blackbox
- Extracted view (optional)
- GDSII
- DRC, LVS, antenna report
- Test bench with saved configurations (optional)
- Documentation